



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/823,386 Confirmation No. 7368  
Applicant : Monte J. Rhodes  
Filed : March 30, 2001  
TC/A.U. : 2645  
Examiner : Elahee, Md S.  
  
Docket No. : 42390P11045  
Customer No. : 008791

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Declaration of Greg D. Caldwell**  
**Pursuant to 37 C.F.R. §1.132**

Sir:

I, Greg D. Caldwell, hereby declare that:

1. I am a citizen of Canada, and currently reside in Newberg, Oregon.
2. I am currently a partner of the law firm Blakely, Sokoloff, Taylor & Zafman.
3. I have worked as an attorney at Blakely, Sokoloff, Taylor & Zafman continuously since July 1995.
4. Drafting of the application for the subject invention was assigned to me on February 2, 2001.
5. I have reviewed the enclosed copy of Monte Rhodes' 37 C.F.R. § 1.131 declaration and the associated invention disclosure (disclosure number 17414; docket number P11045), filed with the response of May 9, 2005, and the docket note assigning the drafting work for the disclosure to me dated February 2, 2001 (please see attached Intel Disclosure

and Foreign Filing Information Form, "Notes"). I certify the copy of the docket note is a true copy of the original and that disclosure 17414 was the basis of the application I drafted and filed on March 30, 2001.

6. I, and Joe Pugh, another attorney then with Blakely, Sokoloff, Taylor and Zafman, pursued drafting of the original application with reasonable diligence in view of our previously existing case load from at least February 2, 2001 until the filing date of March 30, 2001.

7. I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-identified patent application or any patent issued thereon.

Executed on: Oct. 21, 2005  
At: Beaverton, Oregon

By:



Greg D. Caldwell

Matter Status: IN PROCESS

TYPE OF INTEL PATENT APPLICATION FILE\*Patent:  Utility  Design  Reissue  Reexam  CPA (C)  CIP (X)  Divisional (D)Title of File: SERVER APPLIANCE WIRELESS INTERFACEINTEL DISCLOSURE AND FOREIGN FILING INFORMATION

\*Disclosure number(s): 17414

\*Product/Process: COMMON APPLIANCE

Intel Committee: COMMUNICATION SYSTEMS &amp; SOFT

Intel Group: CPG

Intel Division: IMAD

F-Filing: NO

Ctry:

Notes: P11045 (17414) OPENED &amp; ASSIGNED TO GREG CALDWELL PER EMAIL FROM JB 2/2/01 AL.

\*INTEL ABSTRACT CODES (Check One or More)

— PROCESS (C1)	— Buses Input/Output Devices (C5B)	— General Circuit (C14)
— N or P MOS (C1A)	— Protocol/CPU Interfacing (C5C)	— Peripherals (C15)
— Equipment (C1B)	— Adder/Multiplier Units (C5D)	— ROM (C16)
— CMOS (C1C)	— Numeric (C5E)	— Timing Clocks (C17)
— Contacts (C1D)	— Video/Graphics (C5F)	— Power/Regulation (C18)
— Flash (C1E)	— Cache/memory Hierarchy/ (C5G)	— Networks (C19)
— GaAs and SOS (C1F)	— Memory/Virtual Memory (C5H)	— PLD (C20)
— Circuit element (C1G)	— Memory Management/ (C5I)	— Compression/Decompression (C21)
— Isolation/Insulation (C1H)	— Protection/Addressing (C5J)	— Video/Graphics/Audio (C22)
— BiCMOS (C1I)	— Instruction/Inst. Decoding/ (C5K)	— Algorithm (C22A)
— Analysis/Testing (C1J)	— Microcoded/Sequencing/ (C5L)	— System (C22B)
— Etching/Planarization (C1K)	— Microprogrammed Control (C5M)	— Sensor (C22C)
— Metal (C1L)	— Pipeline/Parallelism (C5N)	— Optics (C22D)
— Poly silicon (C1M)	— Clocking/Clock Generation/ (C5O)	— 3D (C22E)
— Passivation (C1N)	— Clock Multiplication (C5P)	— Display (C22F)
— Masking/Resist (C1O)	— Addressing/Addressing Modes (C5Q)	— Graphics Device (C22G)
— Deposition (C1P)	— Vector Processing (C5R)	— Test Equipment (C23)
— Implantation (C1Q)	— Registers/Files/Stacks (C5S)	— Video Teleconferencing (C24)
— DRAMs (C2)	— Multiprocessing/Dual (C5T)	— Communication (C25)
— Sense amp (C2A)	— Initialization/Testing/ (C5U)	— Software (C26)
— SRAMs (C3)	— Debugging (C5V)	— Graphics (C26A)
— Sense amp (C3A)	— Program/Program Control/ (C5W)	— Audio (C26B)
— EPROMS (C4)	— Interrupt/Status/Faults (C5X)	— Compiler (C26C)
— P-channel (C4A)	— Exceptions (C5Y)	— Operating System (C26D)
— N-channel (C4B)	— RISC (CSR)	— Drivers (C26E)
— Flash (C4C)	— Redundancy (C5Z)	— Other (C26F)
— EE (C4D)	— SYSTEMS (C6)	— IAL (C27)
— Sense amp (C4E)	— Bus (C6A)	— Internet/WWW Applications (C27A)
— Solid-State disk (C4F)	— Supercomputers (parallel multiprocessors) (C6B)	— Java Applics. (C27B)
— Flash Card (PCMCIA) (C4G)	— Compilers (C6C)	— User Interfaces Consumer (C27C)
— Multibit Cell (C4H)	— Test Equipment (ICE) (C6D)	— Appliances Portable (C27D)
— Redundancy (C4I)	— BIOS (C6E)	— Computing (C27E)
— Blocking (C4J)	— PCMCIA (thin removable functionality cards, i.e., memory, modem, network, etc.) (C6F)	— Compilers (C28)
— Write Automation (C4K)	— Magnetics (bubble memones) (C7)	— Java Compilers (C28A)
— Minicard (C4L)	— Buffers (C8)	— Java Just-in-Time (C28B)
— Camera (C4M)	— Packaging/Mounting/ Connector (C9)	— IA64 Compilers (C28C)
— FMM (C4N)	— Logic (C10)	— Optimization (C28D)
— Firmware Hub (FWH) (C4O)	— Neural (C11)	— Circuits (C29)
— Security (C4P)	— Miscellaneous (C12)	— New Logic Family (C29A)
— Small Block (C4Q)	— General Memories (C13)	— Data Path (C29B)
— FDI (C4R)	— Redundancy (C13A)	— Chipsets (C30)
— Interface (C4S)	— Rambus-compatible (C13B)	— Memory Control (C30A)
— Connector (C4T)		— Bridging (C30B)
— Cell Phone (C4U)		— Firmware Hub (C30C)
— Charge Pump (C4V)		— Design Tools (C31)
— Audio (C4W)		— Circuits (C31A)
— Microprocessor (C5)		— Layout (C31B)
— Embedded (C5A)		— Logic (C31C)

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\*Mandatory for original patent application. File will not be opened unless mandatory information is provided.

## **\*INTEL ABSTRACT CODES (CONTINUED)**

— CIRCUIT (C32)	— SWITCH/ROUTER (C41)	
— A/D	— ATM	(C41A)
— D/A	— Ethernet	(C41B)
— Amplifier	— MAC	(C41B2)
— OP (Operational)	— PHY	(C41B3)
— RF (Radio Frequency)	— Load Balancer	(C41C)
— Isolator	— XML	(C41D)
— Receiver	— Routing	(C41E)
— Jitter Attenuator	— SECURITY (C42)	
— FM Demodulator	— Cryptography	(C42A)
— Antenna Interface	— Smartcard	(C42B)
— Line Driver	— VPN	(C42C)
— PLL	— Access Control	(C42D)
— Frequency Multiplier	— TELEPHONY (C43)	
— Time Recovery	— Call Control Features	(C43A)
— Filter	— Circuits	(C43B)
— Adaptive	— Fax	(C43C)
— Switched Capacitor	— ISDN	(C43D)
— Equalizer	— Bridge	(C43D2)
— Echo Canceller	— PBX	(C43E)
— Detector	— Video Conferencing	(C43F)
— Signal Generator	— Voice/Speech Processing	(C43G)
— Oscillator		
— TEST		
— BIST (BUILTIN-S-TEST)		
— CODING/MODULATION. (C33)		
— Viterbi	(C33A)	
— Block	(C33B)	
— Trellis	(C33C)	
— FM	(C33D)	
— QAM	(C33E)	
— HUB/REPEATER (C34)		
— Ethernet	(C34A)	
— MAC	(C34A2)	
— PHY	(C34A3)	
— Ring	(C34B)	
— MODEM (C35)		
— Cable	(C35A)	
— DSL	(C35B)	
— PSTN	(C35C)	
— Voice and Data	(C35C2)	
— Wireless	(C35D)	
✗ NETWORK MANAGEMENT (C36)		
— Agent	(C36A)	
— Network Discovery	(C36B)	
— Network Topology	(C36C)	
— Fault Tolerance	(C36C2)	
— Policy Based Management	(C36D)	
— PROXY	(C36E)	
— Software Distribution	(C36F)	
— Virus Protection	(C36G)	
— NETWORK OS (C37)		
— NIC (C38)		
— Architecture	(C38A)	
— Bus Master	(C38A2)	
— ATM	(C38B)	
— Device Driver	(C38C)	
— Ethernet	(C38D)	
— MAC	(C38D2)	
— PHY	(C38D3)	
— Media Attachment	(C38D4)	
— Media Independent Interface	(C38D5)	
— NETWORK PROCESSOR (C39)		
— Multi-threaded	(C39A)	
— Architecture	(C39B)	
— Instruction set	(C39B2)	
— Compiler	(C39C)	
— Bus	(C39D)	
— Memory	(C39E)	
— Micro-architecture	(C39F)	
— Memory Controller	(C39G)	
— Switch	(C39H)	
— Debugging	(C39I)	
✗ NETWORK COMM. _PROTOCOLS (C40)		
✗ Internet	(C40A)	
— Audio or Video	(C40B)	
— Web Caching	(C40C)	
— Bus Method	(C40D)	
— Wireless	(C40E)	
— Home Networking	(C40F)	
— Phone Line	(C40F2)	
— Power Line	(C40F3)	
— Wireless	(C40F4)	